

12-13-04

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Attorney Docket No. 02-CT-239/DP
Client Matter No. 85696.0030

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.	10/677,108	Confirmation No.	4289
Inventor:	Antonino SCHILLACI, et al.	Title:	HIGH PERFORMANCE, INTEGRATED, MOS- TYPE SEMICONDUCTOR DEVICE AND RELATED MANUFACTURING PROCESS
Filed:	September 30, 2003		
TC/A.U.	2811		
Examiner:			
Docket No.	02-CT-239/DP		
Customer No.	25235		

CERTIFICATE OF MAILING BY EXPRESS MAIL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

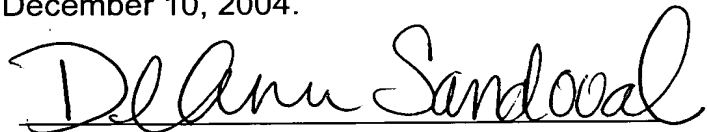
Sir:

The undersigned hereby certifies that the following documents:

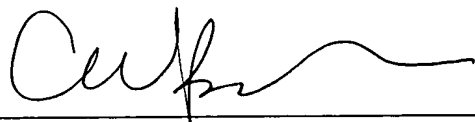
1. Response to Restriction Requirement;
2. Return Postcard, and

this Certificate of Mailing relating to the above application, were deposited as "Express Mail," Mailing Label No. EL533456658US with the U.S. Postal Service, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 10, 2004.

December 10, 2004


Mailer

December 10, 2004


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RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed November 29, 2004, please enter the following amendments.

Amendments to the claims begin on page 2 hereof.

Remarks begin on page 6 hereof.